



POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee, Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

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FACULTY PROFILE

A. Sekhar Sunil
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Vijayawada-520001.



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Email: sunilpscmrcet@gmail.com

Experience:

Teaching:5.5 years

Career Profile

Year	Organization	Role
5 th June 2013to 23 rd May 2018	Andhra Loyola Institute of Engineering & Technology	Assistant professor
9 th July 2019 to till date	Potti Sri Ramulu Chalavadi Mallikharjuna Rao College of Engineering and Technology	Assistant professor

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
M.Tech	JNTUH	10/02/2014	82.7
B.Tech	JNTUK	10/04/2010	76.47
INTERMEDIATE	BIE,AP	01/04/2006	94.4
SSC	SSC,AP	25/03/2004	92

The subjects handled:

**NA, PEE, PS-1, PS-2, EM-1, EM-2, EM-3,CS,PE,STLD,PSA,PSOC,EDS,DCS,
EM-1 LAB, BEE LAB, PE LAB, ES LAB, EMS LAB**

Affiliation to Professional Bodies

MIAENG, MISRD, MITRED

Publications:

1. **“Simulation and Control of a Hybrid Multilevel Converter with Floating DC Links”** in International Journal of Electronics & Communication Engineering (ISSN-2321-5591) in March 2016.
2. **“Simulation of Cascaded H-Bridge Multilevel Inverter for Compensation of Reactive Power and Harmonics Using FACTS Devices”** in International Journal of Electronics & Communication Engineering (ISSN-2321-032X) in March 2016.
3. **A Cascaded H-Bridge Multilevel Inverter Using Switched Parallel DC Voltage Sources in International Journal For Modern trends in science and technology (ISSN- 2455-3778) in March-2017**
4. **A Control Method for UPQC Based on SRF Theory Under balanced and distorted load conditions. (ISSN-2455-3789)**
5. **Simulation of Power Electronic Transformer for Improving Power Quality in International Journal of Modern trends in science and technology (ISSN 2348-3121)**
6. **Enhancement of Power Quality in Grid Connected Isolated Power Systems in RACES-2K16**
7. **Presented a paper titled Development of DC-DC Buck Converter using Arduino in IJMTST/NCEE-2018**
8. **Presented a paper titled Power transformer protection using Fuzzy logic based Differential relay in IJMTST/NCEE-2018**

Conferences, Seminars, Workshops & Guest Lectures Attended

- Participated in a workshop on “Lab View” at DVR & Dr. HS & MIC COLLEGE OF TECHNOLOGY, Kanchikacherla.
- Participated in a workshop on “MATLAB” at Andhra Loyola Institute Of Engineering And Technology, Vijayawada.
- Participated in an **APSSDC FACULTY IMPROVEMENT PROGRAMME** on “SIMOCODE, SIMARIS-SEIMENS” at VVIT-Nambur from 15th May 2017 to 20th May 2017.
- Participated in a Two day National workshop on “Research tool for advanced modelling & Analysis of Electrical Utilities” at University College Of Engineering, JNTUK, Vizianagaram, during 20-21st November 2017

Events organized/ conducted

Ratification Status:

Ratified as an Assistant Professor in the Department of EEE by JNTU Kakinada. on 16/10/2016.

Signature