

POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee, Estd: 1906 (Approved by AICTE Affiliated to JNTU Kakinada) ISO 9001:2015 Certified

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FACULTY PROFILE

Batta. Sivaprasad, s/o: Nagaiah, opp high school, jandrapeta, chirala mandal, prakasam district.

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Experience:

Teaching: NO Industry: NO

Qualification

Course	Discipline	College/School	University/Bo ard	Year of Passing	% of marks
м.тесн	Power System And Control Automatio n	Gayatri Vidya Parishad College Of Engineering (Autonomous)	JNTU Kakinada	2016	75.86%
в.тесн	Electrical & Electronics Engineerin g	Chirala engineering college, chirala	JNTU Kakinada	2012	71.05%
Intermedia te	Physics Chemistry Mathemati cs	Vignana Bharathi junior college	Board of Intermediate Education (AP)	2008	90.80%
Class X	SSC	B.V & B.N High School	Board of Secondary Education (AP)	2006	70.16%

The subjects handled:

1) Power system operation and control (PSOC)

Certifications

YEAR	MARKS	SCORE	ALL INDIA RANK
GATE -2019	79/100	767/1000	938
GATE-2018	51.33/100	595/1000	3053

Publications

1. Batta sivaprasad, "design of power system stabilizer using particle swarm optimization technique", International Journal of New Technology and Research ISSN:2454-4116 volume-IV Issue-II Feb-2018

