



**POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO
COLLEGE OF ENGINEERING & TECHNOLOGY**

Sponsored by: SKPVV Hindu High School Committee, Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

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FACULTY PROFILE

RAJESH VELPULA

S/O DASU VELPULA

D NO 4-8

KAMBHAMPADU POST

VATSAVAI MANDAL

KRISHNA DISTRICT

Mobile: 9912203554

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Experience:

Teaching: 8 + YEARS

Career Profile

YEAR	ORGANIZATION	ROLE
2012 – TILL DATE	POTTI SRIRAMULU CHALAVADI MALLIKARJUNA RAO COLLEGE OF ENGG. AND TECH., VIJAYAWADA.	Assistant Professor
2010 - 2012	MVR COLLEGE OF ENGINEERING & TECHNOLOGY, PARITALA	Assistant Professor

Qualification

Degree	Board / University	Date of Completion	% Marks
M.TECH (POWER SYSTEMS)	JNTU KAKINADA	2011	73.14

B. TECH (EEE)	RVR&JC CE/Acharya Nagarjuna University	2008	66.25
INTER	Board of Intermediate Education, AP	2004	76.10
SSC	Board of Secondary Education, AP	2002	66.33

THE SUBJECTS HANDLED:

- Power System Operation & Control
- Electrical Measurements
- Control systems
- Power System Analysis
- Electrical Machines
- Electrical Circuit Analysis
- Special Electrical Machines.
- Network Analysis
- Switchgear and Protection
- Basic Electrical Engineering

Certifications

Curriculum:

Certification	Date Certified
IIT NPTEL CERTIFICATION in "INTRODUCTION TO SMART GRID"	2018

Extra Curriculum:

Certification
Certificate of "Winners in CRICKET" in 10 th Annual Sports Day Celebrations held on March-2018
Secured third prize in shot put in 10 th Annual Day Celebrations held on march-2018

Affiliation to Professional Bodies

- Member of IAENG
- Member of ISSE
- Life Member of ISTE

- Life Member of ICSES
- Life Member of TERA

Publications

- Increasing the Power System Switch Gear Capacity by Using Superconducting Fault Current Limiter-(IJERGS)-ISSN 2091-2730, Volume 3, Issue 6, November-December, 2015
- Modelling and Design of Series Voltage Compensator for Grid Tie Solar Inverter Using Hybrid Fuzzy Technique – (IJCSM) -ISSN: 2455-1910 - Vol.3.Issue.1.2017.
- “Design and Analysis of Z Source inductor-based circuit breaker for DC application”- International Journal of Emerging Technology and Innovative Research. -ISSN: 2349-5162- Vol.5. Issue 12. 2018.
- “Power system stability analysis under increasing penetration of PV power plants with SPC using double derivative”-(IJR)- ISSN:2236-6124 - Vol.VIII. Issue 4. April,2019.
- “Power quality control of voltage and load sharing in DC distribution systems with Three Phase AC Machines” -(JETIR)-ISSN:2349-5162, Vol.6, Issue.3. March 2019.
- “Hybrid Fuzzy based Unified Power Flow Controller to reduce damping oscillations in multi machine system” -(URJ)-ISSN:2277-2723, Vol.VIII, Issue III, March-2019.
- Intelligent Hybrid Fuzzy controller-based Zeta converter for BLDC motor drive water pumping system fed from PV” -(IJMTE)-Vol. IX, Issue.3, March,2019.
- “Enhancement of AC drive performance by using DCMLI - hybrid series active power filter” - (JASC)-ISSN:1076-5131, Vol.6. Issue 3, March,2019.

Conferences, Seminars, Workshops & Guest Lectures Attended

- “Hybrid Damping Controller for UPFC to enhance power quality in Multi Machine System” in International Conference on Contemporary Engineering and Technology (ICCET) at PSCMRCET, Vijayawada in February 2018.
- “A Voltage controlled DSTATCOM based voltage regulator for low voltage distribution grids” -in International Conference on First International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems (CITAES) at GEC, Gudlavalleru – 30th NOV,2018 & 1st Dec,2018.
- A Five day faculty development program on “Instructional Design & Delivery”-during 4th June,2018 to 8th June 2018 at PSCMRCET, Vijayawada.

- A Five-day workshop on “Effective Teaching and Learning of SMART GRID & MICRO GRID TECHNOLOGIES” during 4th to 8th of December, 2017 at NIT Warangal.
- A Two-day National seminar on “Role of Smart Grids in Smart Cities: Challenges and Solutions” on 15th and 16th September 2017 at DVR & DHS MIC College of Technology.
- A Six-day Training Programme in “Advanced Control Systems” during 23-01-2012 to 28-01-2012 held at VRSEC, Vijayawada organized by AP State council of higher education in association with JNTU, Kakinada.

Events organized/ Conducted

- Organized “Two-day National level workshop on Trouble shooting and Servicing of Electrical Drives and Appliances”, - 17-8-2018 & 18-08-2018 at PSCMRCET.
- Organized as a Department Coordinator for “5 Day workshop on PCB Design” with collaboration of IEI Student Chapter, PSCMRCET held on between 11-09-2018 to 15-11-2018
- Organized as a Department Coordinator for “5 Day workshop on PCB Design” with collaboration of IEI Student Chapter, PSCMRCET held on between 21-08-2017 to 26-08-2017
- Organized as a Department Coordinator for “5 Day workshop on PCB Design” with collaboration of IEI Student Chapter, PSCMRCET held on between 25-11-2016 to 30-11-2016
- Acts as a resource person for three-day workshop on “SCILAB and its applications to electrical engineering” with collaboration of IEI Student Chapter, PSCMRCET- during 30-11-2016 to 02-12-2016.
- Acted as a judge for “POLY TECHFEST-2017” conducted by DEPARTMENT OF TECHNICAL EDUCATION, Govt. of Andhra Pradesh – (08-09-2017 & 09-09-2017).

Signature

RAJESH VELPULA