



**POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO
COLLEGE OF ENGINEERING & TECHNOLOGY**

Sponsored by: SKPVV Hindu High School Committee, Estd: 1906

(Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

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FACULTY PROFILE

RAJESH RAMELLA

S/O KOTESWARA RAO

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Sri Satya Sai Towers, Bodapadu,

Nunna post, Vijayawada rural-521212

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Experience:

Teaching: 5 + Years

Career Profile

YEAR	ORGANIZATION	ROLE
2017 - TILL DATE	POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGG. AND TECH., VIJAYAWADA.	Assistant Professor
2014 - 2017	VIJAYA INSTITUTE OF TECHNOLOGY FOR WOMEN VIJAYAWADA.	Assistant Professor
2013-2014	LINGAYAS INSTITUTE OF TECHNOLOGY VIJAYAWADA.	Assistant Professor

Qualification

Degree	Board / University	Date of Completion	% Marks
PhD	KL university	Pursuing	--
M.TECH (POWER ELECTRONICS)	JNTU KAKINADA	2014	80.73
B.TECH (EEE)	VIGNAN'S ENGINEERING COLLEGE VADLAMUDI (Vignan's University)	2009	72.34
POLYTECHNIC	SBTET, AP	2005	67.68
SSC	BOARD OF SECONDARY EDUCATION, AP	2002	70.14

THE SUBJECTS HANDLED:

- Power Electronics
- Power semi conductor drives
- High voltage Engineering
- Power Systems-II
- Switch gear and protection
- Special Electrical Machines.
- Electrical technology
- Basic Electrical Engineering
- Electrical and mechanical technology

Certifications

Extra Curriculum:

Certification

Certificate of "Winners in CRICKET" in 10th Annual Sports Day Celebrations held on March-2018

Affiliation to Professional Bodies

- Member of IAENG
- Member of IRED
- Life Member of ISTE

Publications

- Published a paper on “Design and Analysis of Z-Source Inductor Based Circuit Breaker for DC. Applications” in **Journal of Emerging Technologies and Innovative Research** an [UGC and ISSN Approved Journal](#) in December 2018. DOI: <http://doi.one/10.1729/Journal.19014>
- Published a paper on “Utilization of PV Array with High Step up DC-DC Converter Applications to Grid Connected System” in International Journal of Electrical and Electronics Engineering (IJEED) in 2013.

Conferences, Seminars, Workshops & Guest Lectures Attended

- Attended 3 day workshop on research methodology at KL university from January 08 to January 10,2019.
- “Published a paper on “Design and Analysis of Z-Source Inductor Based Circuit Breaker for DC. Applications” in International Conference on Contemporary Engineering and Technology (ICCET) at PSCMRCET, Vijayawada in February 2018.

Events organized/ Conducted

- Organized "Two day National level workshop on Trouble shooting and Servicing of Electrical Drives and Appliances", - 17-8-2018 & 18-08-2018 at PSCMRCET.
- Organized as a Department Coordinator for "5 Day workshop on PCB Design" with collaboration of IEI Student Chapter, PSCMRCET held on between 21-08-2017 to 26-08-2017
- Organized 2 Days Technical workshop on "COMPUTER HARDWARE TRAINING" during the period from 21/08/2017 to 22/08/2018. Organized by Dept. EEE in association with IEI student chapter at PSCMRCET, Vijayawada.
- Coordinator for LOGO Competition on Engineers day celebrations held on 15-09-2017. Organized by Dept. EEE in association with IEI student chapter at PSCMRCET, Vijayawada.
- Organized "PSCMRCET JIGNASA-2K18" in January 2018, A NATIONAL LEVEL TECHNICAL SYMPOSIUM.
- Attended Faculty Development Program (FDP) on English communication skills in SRK institute of Technology, Vijayawada.

RAJESH RAMELLA

Signature