

FACULTY PROFILE

B. Mohan Swaroop
S/o : B. Venkateswara Rao
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Experience:

Teaching: 5 Years
Industry: Nil

Career Profile

Year	Organization	Role
2013 to till date	Potti Sriramulu Chalavadi Mallikarjuna Rao college of Engg. And Tech., Vijayawada.	Assistant Professor

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
M.Tech	JNTUK	December 2012	75.1
B.Tech	JNTUK	July 2010	61.3
Intermediate	Board of Intermediate Education	April 2006	67.4
SSC	Board of secondary education	April 2004	76.6

The subjects handled:

1. Electronic Devices and Circuits
2. Switching theory and logic design
3. Electronic measurements and instrumentation
4. Digital signal processing
5. Optical communications
6. Structured digital design
7. Bio medical engineering
8. Data communications
9. Digital IC applications

Certifications

Certification	Date Certified
Instructional excellence in intelligent systems under the INTEL College excellence program.	12 th to 14 th February 2014.

Affiliation to Professional Bodies

1. Indian society of systems for science and engineering (ISSE).
2. International association of engineers IAENG.

Publications

1. Establishing a low bandwidth and low latency transceivers for low dense applications", Vol.3, Issue 9, September 2017, published in international journal for modern trends in science and technology.
2. An Efficient Vedic Square Cube Architecture using Low Power and Less Complex Architectures", Vol.4, Issue:25, ISSN: 4758-4761, published in international journal of scientific engineering and technology.
3. Establishing a Secured and Low Power Transmission in Serial Links", Vol.4, Issue:24, ISSN:2319-8885, published in International journal of scientific engineering and technology research.
4. A Robust Watermarking Technique Based On Dwt on Digital Images, Vol.4, Issue:1, ISSN: 2249-6645, published in International journal of modern engineering research.
5. Optimization of power in hybrid circuits using adiabatic techniques, IJERT, August 2013.

Conferences, Seminars, Workshops & Guest Lectures Attended

1. Intelligent electronic systems design using bio-inspired algorithms, organized by E&ICT Academy, NIT Warangal from 28th October to 2nd November, 2017 (one week).
2. Recent Trends in signal processing organized by E&ICT Academy, NIT Warangal from 31st October to 5th November 2016.
3. Custom IC Design and ASIC implementation using Cadence EDA tools in technical collaboration with Entuple Technologies from 3rd August to 5th August 2016.
4. Advanced antenna design and simulation using HFSS & CST at KKR & KSR Institute of Technology & science from 30th December 2016 to 1st January 2017.
5. CMOS, Mixed signal and Radio Frequency VLSI Design organized by IIT Kharagpur through MHRD at RVR& JC College of Engineering, Guntur from 30th January to 4th February 2017.

Signature

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