



POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee, Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

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FACULTY PROFILE

Donepudi suresh babu

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Experience:

Teaching: 7 years

Industry: 1 year

Career Profile

Year	Organization	Role
2012- till date	PSCMR CET	Asst. Professor
2011-12	Cynamai technologies, Hyderabad.	Project coordinator

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
M.TECH (Computers & Communication)	JNTUK	2011	79.1
B.TECH(ECE)	ANU	2008	75.23
INTER	BIE	2004	88.7
X CLASS	SSC	2002	79.2

The subjects handled:

S.No	Subject	No. of Times taught
1	Digital Image Processing	10
2	Switching Theory & Logic Design	6
3	Basic Electronics & Devices / Electronics Devices Circuits	6
4	Microwave engineering	4
5	Random Variables & Stochastic Processing	2
6	TV Engineering.	2

Certifications

Certification	Date Certified
NPTEL Certificate ON DIGITAL IMAGE PROCESSING	2016
NPTEL Certificate ON Introduction to Internet of Things	2018
NPTEL Certificate ON Outcome Based Pedagogic Principles for Effective Teaching	2018
Appreciation Certificate for simulating Single Tuned Amplifier in Circuit Simulation Project on Esim through IITB,MHRD & FOSSEE	2018
Appreciation Certificate for simulating Voltage Series Feedback Amplifier Circuit Simulation Project on Esim through IITB,MHRD & FOSSEE	2018

Affiliation to Professional Bodies

- IAENG(member of International Association for Engineers, REGN NO-173203)
- ISSE(Indian society of systems for science and engineering, LM 03694)
- MNSPE(Member of National Society of Professional Engineers)
- MICST(Member of Institute for Computer Sciences, Social Informatics and Telecommunications Engineering)

Publications

No	Title of the paper	National /International	Name of the journal	Month & year of publication	DOI
1	Electroencephalogram based open BCI devices for disabled persons	International	Springer LN	Jan 2017	https://doi.org/10.1007/978-981-10-4280-5_24
2	Design of miniature wide band micro-strip antenna	International	Ijert	March 2016	-

Conferences, Seminars, Workshops & Guest Lectures Attended

- ❖ Attended FDP'S At PSCMR CET English Communication Skills(2013), Hands Of Experience With Hfss(2015), Magical Teacher(2016), Instructional Design & Delivery(2018), Ham Radio.
- ❖ Attended QIP short term at IITKGP on "Fundamentals of Digital Image & Video Processing with 3D Applications." From 15th may, 2018 to 19th may, 2018.
- ❖ Attended courseware, practical training and evaluation by INTEL-FICE at PSCMR CET on "Instructional Excellence in intelligent systems." From 12th feb, 2014 to 14th Feb, 2014.
- ❖ Attend in many workshops conducted in Dept. of ECE, PSCMR CET like PCB DESIGN, SCILAB, HFSS every year from 2015.
- ❖ Attended a symposium on Radio Frequency antenna applications and Measurements, at SRM University in 25-26th august, 2014.
- ❖ Participated in two day Workshop on Smart Instrumentation system with solar low and advanced signal processing techniques at LBRC in 30-31st, Dec 2016.
- ❖ Participated a workshop on Speech Signal Processing organised by NITW during 10-12th, MAY 2013.
- ❖ Participated a workshop on 5G technology knowledge and skills by UTL Technologies at PSMCR CET during 24th August, 2018.
- ❖ Participated in MATLAB EXPO – 2017 at Hyderabad.

Events organized/ Conducted

- ❖ ACTED AS A RESOURCE PERSON IN WORKSHOP LIKE PCB DESIGN, SCI LAB, HFSS CONDUCTED IN PSCMR CET.
- ❖ Acted as judge for the District Level Project Exhibition in POLT-TECHFEST- 2017 at PSCMR CET.

Honorariums

Got honorarium for designing circuits using e-sim from MHRD, IITB in association with FOSSEE and got the Appreciation Certificate.

<https://esim.fossee.in/circuit-simulation-project/esim-circuit-simulation-run/157>

<https://esim.fossee.in/circuit-simulation-project/esim-circuit-simulation-run/85>

Signature