POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee,Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)
ISO 9001:2015 Certified
Raghavareddy Street, Kothapeta, Vijayawada-520001.
Andhra Pradesh, India. Phone: 0866 –2423442.
Email: info@pscmr.ac.in; URL: www.pscmr.ac.in

FACULTY PROFILE

A.Ramesh

S/O A.Venkatasubbaiah
Kolladinne Village
Mungamure post
Bogole Mandal
Nellore District.
Mobile: 9177879590
Email: rameshavula99@gmail.com

Experience:

Teaching: 5+ years

Industry: 1 + Years

Career Profile

Year	Organization	Role
2018 -Till date	PSCMRCET, Kothapet, Vijayawada.	Asst.prof
2013-2018	PSCMRCET, Kothapet, Vijayawada.	Asst.prof
2011-2013	Media Systems India PVT.LTD, Cochin, Kerala.	Software Engineer Trainee

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
Pursuing Ph.D (ECE) Antennas	KLUniversity		
M.Tech ECE	JNTU Anantapur	2011	69.25
B.Tech ECE	ANU, Guntur	2009	76.45

Intermediate MPC	Board of Intermediate Education	2005	61.5
SSC	Board of Secondary education	2002	70
D.El.Ed	Sai deepthi College of Elementary Education	ANU	76.45
BLISC	Prabhas College	ANU	74.24

The subjects handled:

1. Bio medical Engineering
2. Satellite Communication
3. VLSI
4. Computer Architecture and Organization

Certifications

Certification	Date Certified
Interview Preparation Guide for Social Media Marketing	Oct 31,2018
Become A Certified Web Developer	Oct 31 2017

Affiliation to Professional Bodies

1. IETE Membership

2. ISSE

3. Ham Radio

Publications

➢ A. Ramesh and J.L. Narayana. IEEE Sponsored 3rd International Conference on Innovations in Information Embedded and Communication Systems , ICIIECS'16 on 17th,18th of March 2016**." Development and Analysis of Elliptic Function Micro strip Low pass Filter using Artificial Neural Networks".** At karpagam College of Engineering, Coimbatore.

➢ A. Ramesh and N.V. Seshagiri Rao. 4th International Conference on Electronics, Signal Processing and Communication Engineering(eSpace-2015),"**Technique for Reduction of Inter Symbol Interference in Ultra Wide Band System**" .At Govt. Engineering College, Thrissur during 9th to 11th December 2015.

➢ A.Ramesh and K.Sreenivasa Rao. 3rd International Conference on Signal Processing, Communication and VLSI Design NCSCV'11 on 6th and 7th May-2011 "**Verilog HDL Implementation of Low Power Audio Sub word Sorter Unit for Security in Audio Streaming Applications**". At Anna University of Technology, Coimbatore.

- A. Ramesh and B. Siva Nageswara Rao. International Conference on Advance Computing Logic, Technology and Science in Association with Institute of Engineering and Research (IER). "**Efficient and Enhanced Carry Select Adder for Multipurpose Applications**". At Anantha Lakshmi Institute of Technology and Sciences, Anantapuram on 11th March 2016.

- A.Ramesh and B. Sivanageswar Rao National Conference on Recent Innovations in Engineering and Technology (RIET 2017) **Review of Fractal Antennas and it's Applications in Microstrip Antenna** at G.Pullaiah College Kurnool, AP. On 15th &16Dec-2017

Seminars, Workshops & Guest Lectures Attended

- ❖ Recent Advancements in VLSI Technology & Design Using EDA Tools.(20-24 July 2016)
- ❖ New Algorithms for Signal Processing Hands-on (20-27 April 2018)
- ❖ Advanced Trends in Signal Processing & Application(Jan 29 to Feb 3)
- ❖ A Three Day FDP on "Role of Faculty in Institutional Buildings"

Events organized/ Conducted

Organized as a Coordinator of PCB Design for PSCMRCET From Past 4 years

Funding Projects Handled (If any – Please provide the details of the project)

A.Ramesh

Signature