

POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee,Estd: 1906 (Approved by AICTE Affiliated to JNTU Kakinada) ISO 9001:2015 Certified

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FACULTY PROFILE

S Pradeep Kumar

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Email: satpradeep@gmail.com



Experience:

Teaching: 10 Years

Industry: --

Career Profile

Year	Organization	Role
01-11-2018 to till date	Potti Sriramulu Chalavadi Mallikharjuna Rao College of Engineering and Technology	Associate Professor
06-06-2011 to 30-10-2018	Potti Sriramulu Chalavadi Mallikharjuna Rao College of Engineering and Technology	Assistant Professor
12-12-2007 to 15-04-2011	Murthy Institute of Science & Technology	Assistant Professor
10-06-2005 to 11-12-2007	Murthy Institute of Science & Technology	Lab Assistant

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
M.E.	Osmania University	2012	66
B.Tech.	JNTUH	2003	51
Intermediate	SKNR Govt. Jr. College	1999	57
SSC	Sharada Vidya Nilayam	1995	76

The subjects handled:

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1	Electronic Circuit Analysis
2	Pulse & Digital Circuits
3	Linear IC Applications
4	Digital Logic Design
5	DSD & DICA
6	Signal & Systems
7	Digital Signal Processing
O	Telecommunication Switching
8	Systems & Networks
9	Digital Communications
10	Electromagnetic Fields and
	Transmission Lines
11	Antennas and Wave
11	propagation
12	Data Communication
13	Probability Theory and
	Stochastic Process
14	Linear & Digital IC
14	Applications
15	Digital Image Processing

Certifications

Certification	Date Certified
e-yantra Task Based Training by IIT Bombay	26-June-2016

Affiliation to Professional Bodies

- o IEEE
- o ISTE(life time)

Publications

- o Paper titled "Electroencephalogram-Based Open BCI Devices for Disabled People" is published in Springer Conference Proceedings(ICMEET-2016) on 7th September 2017.
- Paper titled "Smart Health Monitoring System of Patient through IoT" is published in IEEE Conference Proceedings(I-SMAC-2017) on 5th October 2017.
- o Paper titled "Automatic Oxygen Level Control of Patient Using Fuzzy Logic and Arduino" is published in IEEE Conference Proceedings(ICBDACI-2017) on 5th October 2017.

Conferences, Seminars, Workshops & Guest Lectures Attended

- Advances in Digital Image Processing & Lidar from UGC Academic Staff College JNTU Hyderabad.(21 Days)
- Recent Advances in Communication and Signal Processing from Osmania University Hyderabad.(2 Days)
- Telecommunication Switching Systems and Networks from CVR College of Engineering affiliated to JNTU Hyderabad.(7 Days)

Events organized/ Conducted

o IEEE Workshop on Arduino & its Applications.

Funding Projects Handled (If any - Please provide the details of the project)

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Signature S Pradeep Kumar