POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO
COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee,Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)
ISO 9001:2015 Certified
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FACULTY PROFILE

Gollapudi Mary Grace Madhuri
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4th lane,Ayodhya Nagar,
Vijayawada – 520 003.

Mobile: 7207893138
Email: madhuri3b81@gmail.com/ gmgmadhuri@pscmr.ac.in

Experience:

Teaching: 13 years

Industry: NIL

Career Profile

Year	Organization	Role
2013-Till date	Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering and Technology, Vijayawada	Assoc. Professor
2012-2013	Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering and Technology, Vijayawada	Asst. Professor
2004-2012	NIMRA College of Engineering & Technology,Vijayawada	Asst. Professor

Qualification

Degree	Board / University	Date of Completion	% Marks

M.Tech	Nimra college of Engg&Tech.,JNTUK	2010	73
B. Tech.	JNTUH	2003	64
Intermediate	Board of Intermediate Education	2000	59.9
SSC	Board of Secondary School of Education	1998	68

The subjects handled:

1. Switching Theory and Logic Design
2. DSD &Digital IC Applications
3. VLSI Design
4. Microprocessors and Microcontrollers
5. Low Power IC Design
6. Signals & Systems

Certifications

Certification	Date Certified

Affiliation to Professional Bodies

1. IAENG Membership
2. ISSE Life Membership

Publications

- G.M.G.Madhuri,B.Praveen kitti,Dr.N.Prashanti Kumari, "Line based successive cancellation decoder for line complexity" in AISC Springer Book series ,DOI: 10.1007/978-981-10-5903-2 ,pg.no:534-537..

- Published a paper in NCVSPICE conference titled " I2C Master Bus Controller Implementation on FPGA with RAM as Slave "held at JNTU Kakinada.

Books

Conferences, Seminars, Workshops & Guest Lectures Attended

- ❖ Advanced CMOS VLSI organized by E&ICT Academy ,NIT Warangal from 3/11/18 to 8/11/18.

- ❖ Attended "A Two day IEEE Workshop on LEADERSHIP OPPURTUNITIES FOR PROFESSIONAL WOMEN" from 27 to 28 October 2018 AT EFFTRONICS SYSTEMS PVT. LTD.
- ❖ Attended "A Workshop on DIGITAL TWINS" on 12 OCTOBER 2018, PSCMRCET, VIJAYAWADA
- ❖ Resource person for "A 5 day Workshop on PCB DESIGN" from 11 to 15 September 2018, PSCMRCET, Vijayawada
- ❖ Recent trends in IC Technology organized by E&ICT Academy ,NIT Warangal from 16/4/2018 to 21/4/18
- ❖ 3 day workshop on SCILAB, PSCMRCET and resource Person. 29/06/2017 to 01/07/2017
- ❖ Workshop on IOT, PSCMRCET. 24/07/2017 to 29/07/2017
- ❖ A 5 day workshop on PCB DESIGN, PSCMRCET. 21/08/2017 to 26/08/2017
- ❖ IEEE National Workshop on " Pattern based Competence Building" by Ravi Shankar, PSCMRCET. 29/11/2017
- ❖ Fevicryl Workshop, PSCMRCET. 20/12/2017 to 23/12/2017.
- ❖ Participated in Conference of ICCET-2018, PSCMRCET. 02/04/2018 to 03/04/2018.

Events organized/ Conducted

1.A guest lecture by Dr.K.V.Sridhar,Assoc.professor,NIT warangal on "Trends and Challenges in VLSI system design" ,1st September-2015.

2.organised three day workshop on "Hands on Experience-Novel painting" from 20/12/2017 to 23/12/2017.

Funding Projects Handled (If any – Please provide the details of the project)

G.M.G.Madhuri

Signature