POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY
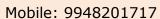
Sponsored by: SKPVV Hindu High School Committee,Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)
ISO 9001:2015 Certified
Raghavareddy Street, Kothapeta, Vijayawada-520001.
Andhra Pradesh, India. Phone: 0866 –2423442.
Email: info@pscmr.ac.in; URL: www.pscmr.ac.in

FACULTY PROFILE

Y.KUMARI
1-3-26/19
Ganji Yallamandaiah street
Vidhyadhara Puram
VIJAYAWADA

Mobile: 9948201717
Email: kumarinvsr@gmail.com

Experience:

Teaching: 29yrs

Industry: ---

Career Profile

Year	Organization	Role
2010-Till Date	**P.S.C.M.R. COLLEGE OF ENGINEERING TECHNOLOGY, VIJAYAWADA-1**	**Professor**
2009-2010	**Krishna Chaitanya Institute of Technology & Sciences, Markapur , Prakasam**(Dist.)	**Professor**
2007-2009	**SVVSN Engineering college, Ongole, Prakasam**(Dist.)	**Associate Professor**

2004-2007	**Sri Vasavi Engineering college, Tadepalligudem, West Godavari** (Dist.)	**Associate Professor**
2000-2004	**Dr. Paul Raj Engineering College, Bhadrachalam, Khammam** (Dist.)	**Assistant Professor**
1989-2000	**S.V.K.P. Polytechnic, Cumbum**, Prakasam (Dist.)	**Associate Lecturer**

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
M.TECH(DSCE)	JNTUH	2006	80
B.TECH (ECE)	ANU	1988	70.6
INTERMEDIATE	INTERMEDIATE	1984	88
SSC	SSC	1982	70

The subjects handled:
1. ELECTRONIC DEVICES AND CIRCUITS
2. ELECTRONIC CIRCUIT ANALYSIS
3. PULSE AND DIGITAL CIRCUITS
4. ANALOG COMMUNICATIONS
5. DATA COMMUNICATION
6. TELEVISION ENGINEERING
7. MICRO PROCESSORS AND MICROCONTROLLERS
8. LINEAR IC APPLICATIONS
9. DIGITAL IC APPLICATIONS
10. DIGITAL IMAGE PROCESSING
11. VLSI DESIGN
12. ELECTONIC INSTRUMENTATION
13. DESIGN OF FAULT TAULERENT SYSTEMS
14. DIGITAL ELECTRONICS
15. ELECTRONIC COMPONENTS

Certifications

Certification	Date Certified
TRAINING PROGRAM ON "HAM RADIO"	2016
IIT NPTEL CERTIFICATION in "DIGITAL CIRCUITS AND SYSTEMS"	2015
IIT KHARAGPUR CERTIFICATION IN "ANALOG ELECTRONICS"	2013
H-PCU PROJECT FOR MATERIAL PREPARATION FOR "ELECTRONIC COMPONENTS"	2000

Affiliation to Professional Bodies

INDIAN SOCIETY OF SYSTEMS FOR SCIENCE & ENGINEERING (ISSE)

IEEE

IAENG

SDIWC

Publications

Sl.No	Title	Name of Publication/Conference	Remarks
1.	Unsharp Masking-Based Image Enhancement of Nano dispersed 10O.O6 Liquid Crystal Compound.	Innovations in Electronics and Communication Engineering pp 23-30; Springer, Singapore	**DOI** https://doi.org/10.1007/978-981-10-8204-7_3 29 August 2018
2.	Noise Removal in Image by Soft Thresholding Technique.	ARPN Journal of Engineering and Applied Sciences	VOL. 11, NO. 23, DECEMBER 2016 ISSN 1819-6608
3.	Design and Analysis of Printed Dual Band Planar Inverted Folded Flat Antenna for Laptop devices	Far East Journal of Electronics and communications	Vol:16,No:1,January 2016 ISSN 0973-7006
4.	A Novel Method Avoids the Falling-Off Boundaries by Using Try-Way Pixel Value Difference and Modulus Function in Image Steganography.	International Journal of Computer Science and Mechatronics (IJCSM)	vol.1.Issue.2 2015
5.	Performance evaluation and comparison of different multi-resolution transforms for image fusion	"National Conference on Innovations and Design Challenges in Electrical & Medical Electronics"	held on 21st & 22nd August 2015 at Gokaraju Rangaraju Institute of Engineering and Technology, Hyderabad.
6.	UART realization with BIST architecture using VHDL	International Journal of Engineering Research and Applications	Vol.3,Issue Jan-Feb 2013. ISSN:2248-9622

Conferences, Seminars, Workshops & Guest Lectures Attended

- Coordinator for 3 Day IEEE workshop on Arduino and Applications.2017
- Attended a 5 Day FDP program on recent trends in Signal Processing by NIT, Warangal.NOV, 2016.
- Attended hands on Experience workshop on "Arc GIS WEB Maps" organized on 16th November 2016
- Attended 1-day training Program on Academy of HAM RADIO Communication Exploration on 31st July 2016, PSCMRCET
- Resource person in a 5- day training Program on Hands on Experience MATLAB on 5th –10th February 2016, PSCMRCET
- Attended a 5 Day FDP program on recent trends in Signal Processing by NIT, Warangal.NOV,2016.
- Resource person in a 3-day work shop on "PCB Design organized by Electronics Enthusiastic club in PSCMRCET from 10-12 September 2015
- Attended 1-day seminar on Trends and Challenges on VLSI System Design held on 1st September 2015, PSCMRCET
- Attended 2 week ISTE workshop on Analog Electronics conducted by Indian Institute of Technology Kharagpur
- Attended 2-day workshop on **VHDL** in Kakatiya Institute of Technology & Science, Warangal, A.P. from 17-18 February, 2003
- Attended 1 week Faculty Development Program (FDP) on **Hands on Experience with HFSS.**
- Attended workshop on Innovative Teaching Techniques at KBN College.
- Attended 1-day work shop on VLSI design in Dr.George Samuel Institute of Technology & Science, Markapur on Feb'2010.
- Participated on H-PCM project for preparation of course material (H-11) Electronic Components in S.V.K.P. Polytechnic College, Cumbum.
- Participated in the seminar on "Trends and Challenges on VLSI System Design " held at Potti Sriramulu CMR College of Engineering and Technologies.
- Participated in **IEEE** National Workshop on "Understanding 5G: Perspectives on Future Technological Advancements in wireless communication " at PSCMRCET.
- Participated and completed INSTRUCTIONAL EXCELLENCE IN INTELLEGENT SYSTEMS under the **INTEL** College Excellence Program.
- Attended 3 Day workshop on PCB design organized by Electronics Enthusiasts Club.
- Participated in 5 day Faculty Development Program on English Communication Skills conducted at PSCMRCET.

Events organized/ Conducted

COORDINATOR FOR IEEE WORKSHOP ON ARDUINO APPLICATIONS AT PSCMRCET,2017
COORDINATOR FOR WORKSHOP ON PCB DESIGN,2015
COORDINATOR FOR PROJECT EXPO,2016

<u>Funding Projects Handled</u> (If any – Please provide the details of the project)
--

Y.Kumari

Signature