# **Curriculum Vitae**

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#### Experience:

Teaching: 10 Years

Industry: Nill

#### **Career Profile**

Year	Organization	Role
05-07-2023 to till date	PSCMR CET,	Assistant Professor
	VIJAYAWADA	
27-12-2013 to 03-07-2023	Sai Spurthi Institute of Technology	Assistant Professor

# **Qualification**

Degree and Date	Board / University	Date of Completion	% Marks
M.Tech (ECE)	JNTUH, Hyderabad	December 2013	78.25%
B.Tech(ECE)	JNTUH, Hyderabad	April 2011	76.09%
Intermediate(MPC)	IPE Board	April 2007	91.5%
SSC	Board of SSC	March 2005	88.8%

## The subjects handled:

- Digital Signal Processing
- Switching Theory and Logic Design
- Electronic Measurements and Instrumentation
- Microprocessors & Microcontrollers
- Data Communications & Networks
- Pulse and Digital Circuits
- Linear and Digital IC Applications
- Control Systems
- Optical Communications
- Telecommunication Switching Systems and Networks
- Cellular and Mobile Communication
- Signals and Systems
- Antennas and Propagation

## **Certifications**

Certification	Date Certified

Affiliation to Professional Bodies
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IAENG membership number is: 272566

#### **Publications**

- A Paper entitled as "Solar powered E-Bicycle" published in ZKG International journal, Volume VIII, Issue I, June/2022.
- A Paper entitled as "Smart waste monitoring and controlling for swach barath using IOT" published in International journal of analytical and experimental modal analysis, Volume XIV, Issue VI, June/2022.
- A Paper entitled as "Gas leak detection and controlling based on wireless sensor network" published in International journal of analytical and experimental modal analysis, Volume XIV, Issue VI, June/2022.
- A Paper entitled as "The embedded systems and data analytics revolution" published in Journal of Interdisciplinary Cycle Research, Volume XII, Issue III, March/2020.
- A Paper entitled as "Three stage Toll Gate Alarming Mechanism on Road Highways" published in International Journal of Computer Applications (IJCA), Vol. 143, Issue 12 June2016.
- A Paper entitled as "An Improved GUI based Voice Identification with Applied RASTA-PLP" published in International Journal of Technology and science, Vol.6(1): 1-3,2015.
- A Paper entitled as "An Electronic Security Monitoring System Using GSM Communication" published in Journal of Academia and Industrial Research (JAIR), Vol. 4, Issue 1 June2015.
- A Paper entitled as "Eco-friendly Environment with RFID Communication Imparted Waste Collecting Robot" published in Journal of Academia and Industrial Research (JAIR), Vol. 4, Issue 2 July 2015.

#### Conferences, Seminars, Workshops & Guest Lectures Attended

- Participated in a One Week Online Faculty Development Program on "Antennas for 5G Communications & Beyond" organized by Dept.of ECE, Aditya Engineering College in association with IETE from 3<sup>rd</sup> -8<sup>th</sup> July, 2023.
- Participated in a One Week Online Faculty Development Program on "Emerging Trends in Artificial Intelligence" organized by Bhimavaram Institute of Engineering & Technology and Blackbuck Engineers Pvt Ltd from 25-31 May, 2022.
- Participated in One Week Online Faculty Development Programme on, "Machine Learning Algorithms for Dead Time Systems (MLADTS)", organized by the Department of Electronics and Communication Engineering, Geethanjali College of Engineering and Technology, Hyderabad, during 24 - 29 January 2022.
- Attended Online webinar on Recent Trends in ECE & IOT on 11-12-2020 at Malla Reddy Engineering College and Management Sciences, Medchal, Hyderabad.
- Attended Online Faculty Development Program on Recent Trends in IOT using Embedded Systems from 07-12-2020 to 19-12-2020 organized by ECE Dept., MGIT, Hyderabad.
- Participated in IEEE workshop on Microwave and Antenna Applications in 5G from 08-12-2020 to 13-12-2020, jointly organized by AP-MTT chapter Bangalore and IEEE Bangalore section.
- Participated in Awareness webinar on outcome based Education Accreditation on 25-11-2020 organized by NBA, New Delhi in association of JNTU Hyd.
- Attended Faculty development Program on System Design Aspects in IOT from 26 -06-2018 to 02-07-2018 at JNTUH Hyd. Attended Faculty Updation Program on VLSI Design organized by C-DAC from 02-06-2014 to 07-06-2014 at C-DAC Hyd.
- Attended the workshop on Signals and Systems under the National Mission on Education through ICT (MHRD, Govt, of India) from 02-02-2014 to 12-02-2014 in Mother Teresa Institute of Science and Technology (RC ID-1102) by IIT Kharagpur.

## **Events organized/ Conducted**

Acted as a Main Coordinator for Medhospurthi 2K18, Technical Students National Symposium which was organized by ECE Dept. at Sai Spurthi Institute of Technology, Sathupally.

# <u>Funding Projects Handled</u> (If any – Please provide the details of the project)

Signature